Joseph B Antony

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SUMMARY

Backend Software Engineer with 4+ years of experience building scalable microservices and cloud infrastructure. Proficient in Java (Spring Boot), RESTful API development, containerization (Docker/Kubernetes), and cloud platforms (AWS, GCP). Passionate about designing high-performance distributed systems and implementing CI/CD for rapid delivery.

EDUCATION

Stony Brook University — **Stony Brook, NY**
M.S. in Computer Science — *2024 – 2026*

Visvesvaraya Technological University — **Bangalore, India**
B.E. in Information Science — *2016 – 2020*

TECHNICAL SKILLS

Languages: Java, Go, C++,C, Rust, Ruby, Python
Tech: Spring Boot, RESTful APIs, Microservices, Hibernate/JPA, Ruby on Rails, Junit, Mockito
Databases: MySQL, Oracle, MongoDB, Oracle SQL DB, Redis.
Cloud/DevOps: AWS, GCP, Docker, Kubernetes, CI/CD

WORK EXPERIENCE

Sigmoid Analytics — **Bengaluru, India**
Senior Software Engineer — *Apr 2024 – Jul 2024*

- Developed microservices for real-time data processing using Spring Boot and Kafka on AWS, reducing latency by 40%.
- Spearheaded containerization (Docker) and CI/CD automation (Jenkins), reducing release cycle time by 50%.

Oracle Cerner — **Bengaluru, India**
Software Engineer – Senior Software Engineer — *Jul 2020 – Mar 2024*

- Developed microservices using Spring Boot, delivering RESTful APIs to thousands of users with 99.9% availability.
- Led migration of a monolithic application to containerized microservices, improving scalability and maintainability.
- Optimized Oracle database by refactoring SQL queries and adding indexes, reducing query time by 30% on key endpoints.

PROJECTS

- **Distributed System**, Built and evaluated a Multi-Paxos + 2PC sharded datastore, implementing WAL recovery and distributed locking; analyzed performance impact of cross-shard coordination on latency and throughput.
- **Datacenter PCIe Accelerator Co-Simulation Platform**, Developed a QEMU–Vivado co-simulated PCIe accelerator (DMA/MSI-X) with NIC bridge and ZeroMQ transport, enabling reproducible host–FPGA verification without RTL changes.
- **Custom KProbe Tool**, Designed and implemented custom monitor program using kprobe to measure context switches, interrupt calls, simulate job Scheduler tree using virtual runtime.
- **University Simple C Compiler**, Simple-C compiler with lexical analysis, parsing, semantic checks, and LLVM-style IR generation, supporting control flow, functions, and type checking with multi-pass optimization and x86 code emission.
- **OMPI**, Open source contribution to OMPI repository.